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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/769,127	01/30/2004	Ping Mei	200209576-1	8740
HEWLETT PACKARD COMPANY P O BOX 272400, 3404 E. HARMONY ROAD			EXAMINER	
			TRAN, THANH Y	
	JAL PROPERTY ADMINISTRATION NS, CO 80527-2400		ART UNIT	PAPER NUMBER
	,		2822	
			MAIL DATE	DELIVERY MODE
			10/17/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

1	Application No.	Applicant(s)				
	10/769,127	MEI, PING				
Office Action Summary	Examiner	Art Unit				
	Thanh Y. Tran	2822				
The MAILING DATE of this communication	n appears on the cover sheet wi	th the correspondence address				
Period for Reply						
A SHORTENED STATUTORY PERIOD FOR R WHICHEVER IS LONGER, FROM THE MAILIN  - Extensions of time may be available under the provisions of 37 C after SIX (6) MONTHS from the mailing date of this communicati  - If NO period for reply is specified above, the maximum statutory  - Failure to reply within the set or extended period for reply will, by Any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b).	NG DATE OF THIS COMMUNIC FR 1.136(a). In no event, however, may a re on. period will apply and will expire SIX (6) MON' statute, cause the application to become AB	CATION.  apply be timely filed  THS from the mailing date of this communication.  ANDONED (35 U.S.C. § 133).				
Status		•				
1) Responsive to communication(s) filed on	<u>29 June 2007</u> .					
2a) ☐ This action is <b>FINAL</b> . 2b) ⊠	This action is non-final.					
3) Since this application is in condition for al	lowance except for formal matte	ers, prosecution as to the merits is				
closed in accordance with the practice un	der <i>Ex parte Quayle</i> , 1935 C.D	. 11, 453 O.G. 213.				
Disposition of Claims						
4)⊠ Claim(s) <u>1-30</u> is/are pending in the applic	ation.					
	4a) Of the above claim(s) 11,12,21 and 24-30 is/are withdrawn from consideration.					
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1,3,13,14 and 17</u> is/are rejected						
7)⊠ Claim(s) <u>4-10, 15-16, 18-20, and 22-23</u> is	/are objected to.					
8) Claim(s) are subject to restriction a	and/or election requirement.					
Application Papers						
9) The specification is objected to by the Exa	aminer.					
10) The drawing(s) filed on is/are: a)	<u> </u>	by the Examiner.				
Applicant may not request that any objection t						
Replacement drawing sheet(s) including the c	orrection is required if the drawing(	s) is objected to. See 37 CFR 1.121(d).				
11) The oath or declaration is objected to by the	he Examiner. Note the attached	Office Action or form PTO-152.				
Priority under 35 U.S.C. § 119	•					
12) ☐ Acknowledgment is made of a claim for fo a) ☐ All b) ☐ Some * c) ☐ None of:	reign priority under 35 U.S.C. §	119(a)-(d) or (f).				
1. Certified copies of the priority docu	ments have been received.					
2. Certified copies of the priority docu	ments have been received in A	pplication No				
<ol><li>Copies of the certified copies of the</li></ol>	e priority documents have been	received in this National Stage				
application from the International B						
* See the attached detailed Office action for	a list of the certified copies not	received.				
Attachment(s)						
1) Notice of References Cited (PTO-892)		ummary (PTO-413) )/Mail Date				
<ol> <li>Notice of Draftsperson's Patent Drawing Review (PTO-94</li> <li>Information Disclosure Statement(s) (PTO/SB/08)</li> <li>Paper No(s)/Mail Date</li> </ol>		formal Patent Application				

### **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1 and 13 are rejected under 35 U.S.C. 102(b) as being anticipated by Dai (U.S. 5,877,076).

As to claim 1, Dai discloses in figures 3i-3k a method for forming a semiconductor device comprising: forming a 3-dimensional pattern (161', 151') in a substrate (110); and depositing at least one material (180) over the substrate (110) in accordance with desired characteristics of the semiconductor device.

As to claim 13, Daidiscloses in figures 3i-3k a system for forming a semiconductor device comprising: means for forming a pattern (161', 151') in a substrate (110) wherein the pattern is 3-dimensional; and means for depositing at least one semiconductor material (180) over the substrate (410) in accordance with desired characteristics of the semiconductor device.

## Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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4. Claims 3, 14 and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Dai (U.S. 5,877,076) in view of Taussig et al (U.S. 6,861,365).

As to claims 3, 14, and 17, Dai does not disclose the semiconductor device comprises a cross-point memory array or at least one of a transistor, a resistor, a capacitor, a diode, a fuse and an anti-fuse.

Taussig et al discloses in figures 1, 2a-2b, and 4a-5 a method for forming a semiconductor device, wherein the semiconductor device comprises a cross-point memory array (see col. 3, lines 61-67); or at least one of a transistor, a resistor, a capacitor, a diode, a fuse and an anti-fuse (see claim 8 and col. 4, lines 14-20). Therefore, it would have been obvious to a person having ordinary skill in the art at the time the invention was made to modify the apparatus of Dai by having a semiconductor device which comprises a cross-point memory array or at least one of a transistor, a resistor, a capacitor, a diode, a fuse and an anti-fuse as taught by Taussig et al for the purpose of intended use.

#### Allowable Subject Matter

5. Claims 4-10, 15-16, 18-20, and 22-23 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

#### Response to Arguments

6. Applicant's arguments with respect to claims 1, 3, 13-14 and 17 have been considered but are most in view of the new ground(s) of rejection.

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# **Contact Information**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thanh Y. Tran whose telephone number is (571) 272-2110. The examiner can normally be reached on M-F (9-6:30pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Zandra Smith, can be reached on 571-272-2429. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

**TYT** 

Zandra V. Smith
Supervisory Patent Examiner

150ct. 2007